

Second

Please amend the ~~third~~ full paragraph on page 23 as follows:

SB 5-26-09

Lowered potential V27 falls to $(V_I - V_{TN} - |V_{TP}|)$ owing to a current flowing from node N27 to the line of fifth power supply potential $[[VO]]$ V5 through N-type transistor 70.

However, N-type transistor 70 is configured to have a small current drive capability for low power consumption. Therefore, a time required for lowering potential V27 of node N27 to the original level of $(V_I - V_{TN} - |V_{TP}|)$ is longer than a time required for raising potential $[[V22]]$ V27 to the same level of $(V_I - V_{TN} - |V_{TP}|)$

Please amend the first paragraph on page 26 as follows:

A drive circuit 90 in Fig. 24 is substantially the same as drive circuit 80 in Fig. 20 except for that signal ϕ_P is applied to the source of P-type transistor 24 instead of ground potential GND, and signal ϕ_P is applied to the drain of N-type transistor 26 instead of fourth power supply potential $[[VO]]$ V4. In this modification, when P-type transistor 81 is on, the drain of P-type transistor 24 attains the "H" level so that flowing of a through-current through transistors 81, 23 and 24 can be prevented. Also, when N-type transistor 82 is on, the drain of N-type transistor 26 attains the "L" level so that flowing of a through-current through transistors 26, 27 and 82 can be prevented. Accordingly, current consumption of circuits 61 and 63 can be reduced.

Please amend the first paragraph on page 29 as follows:

This sixth embodiment can achieve the same effect as the fifth embodiment, and further can reduce the current consumption because it is possible to reduce the current flowing from the line of third power supply potential V3 to ground potential GND through transistors 97, 99, 100